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Press Contacts:

Eileen Elam
KJ Communications (in the U.S.)
Tel: +1 408 927-7753
eileen@kjcompr.com

Nick Foot
Billings PR (in Europe)
Tel: +44 1491 636393
nick.foot@billings-europe.com

Innovative Silicon's Serguei Okhonin Named EE Times ACE Awards "Innovator of the Year" Finalist

Z-RAM Gen2 memory technologist recognized by media leader

SANTA CLARA, Calif., January 19, 2007— Innovative Silicon Inc. (ISi), the developer of Z-RAM[®] high density memory intellectual property (IP), today announced that Dr. Serguei Okhonin, the company's chief scientist and co-founder, was named this week as one of the 2007 EE Times' ACE Award Innovator of the Year Finalists. *EE Times*, published by CMP Technology Electronics Group, has chosen just five finalists in this category and will name the winner at an awards gala at the Fairmont Hotel in San Jose, CA on April 3.

Dr. Okhonin is being recognized for his recent contributions toward the development of Z-RAM Gen2, the company's second generation Z-RAM technology that was announced in December http://www.innovativesilicon.com/en/pdf/gen2_rollout.pdf. The product, offered as IP, delivers significant performance improvements with greatly reduced power consumption over its predecessor. Like the original Z-RAM, Z-RAM Gen2 offers up to twice the density of embedded DRAM and is five times denser than embedded SRAM. This combination of extreme density, high performance, and low power consumption makes Z-RAM Gen2 an ideal memory for most IC applications, including low-power applications that require long-battery life.

ISi is being recognized with a number of prestigious awards. Earlier this month, the company was profiled as one of five winners, and the only semiconductor company, in *IEEE Spectrum Magazine's* "Winners and Losers 2007" listing (see <http://spectrum.ieee.org/jan07/4839>, and <http://spectrum.ieee.org/jan07/4830>). *EE Times* has also named ISi in its list of 60 Emerging Startups - better known as the "Silicon 60" - each year since the list was created in early 2004. ISi

was also awarded the “Best Invention of the Year” status by the Swiss Federal Institute of Technology (EPFL) in 2002.

About Dr. Serguei Okhonin

Dr. Okhonin received his M. Sc. in Physics from Novosibirsk State University in 1980, and his PhD degree from the Swiss Federal Institute of Technology (EPFL) in 2001. From 1980 to 1993, he worked as Research Associate at the Institute of Semiconductor Physics, Novosibirsk, Russia, focusing on semiconductor device physics. From 1994 to 2003, he worked as Research Associate at l’Institut de Micro et Optoélectronique (IMO) and Laboratoire d’Electronique Générale (LEG) at Ecole Polytechnique Fédérale de Lausanne (EPFL). He took part in several European projects focused on advanced CMOS technology development. In 2002 he co-founded Innovative Silicon, developing a new SOI single transistor memory technology. He has authored or co-authored more than 50 papers and filed more than 30 patents. Dr. Okhonin has served as member in program committees of the ASDAM and ESSDERC international conferences.

About Innovative Silicon

Innovative Silicon Inc. (ISi) delivers ultra high density memory IP for embedded SoC, MPU, and portable consumer applications. Endorsed by *IEEE Spectrum Magazine* in 2007 as one of five ‘winning’ technologies, ISi’s Z-RAM® memory provides up to twice the density of embedded DRAM and is five times denser than embedded SRAM, making it the world’s lowest-cost semiconductor memory solution. The company closed its first round of VC funding in 2003, completed its first 90nm megabit Z-RAM memory designs in 2004, its first 65nm designs in 2005 and its first 45nm designs in 2006. The company is incorporated in the USA with R&D in Lausanne, Switzerland. For more information see <http://www.z-ram.com>.

About the CMP Technology Electronics Group

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